



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

**Appl. No.** : **09/377,740**  
**Applicant** : **Kimikatsu SHOJI et al.**  
**Title** : **SEMICONDUCTOR INTEGRATED CIRCUIT  
AND METHOD OF MANUFACTURING THE SAME**  
**Filed** : **August 20, 1999**  
**Art Unit** : **2811**  
**Examiner** : **S. CRANE**  
**Docket No.** : **32014-150502**  
**Customer No.** : **26694**  
**Confirmation No.** : **8263**  
**Date** : **January 15, 2004**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

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**AMENDMENT**

In response to the *Ex Parte Quayle* Action dated December 30, 2003, Applicants submit the following Amendment and Reply. The Application is amended as follows:

**Amendments to the Claims** are reflected in the listing of the claims which begins on page 2.

**Remarks** begin on page 4.

No fees are necessary. However, if the Patent Office deems that fees are required, authorization is hereby granted to charge any required fees to Deposit Account No. 22-0261.